

Serial No. 10/646,936

Docket No. 200210023-1

LISTING OF THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Cancelled)

2. (Currently Amended) A system for providing a temporarily modified output, comprising:

a waveform control that provides a control output that temporarily adjusts to an intermediate level during a transition between normal high and low levels during a first operating mode, the waveform control provides the control output to transition periodically between the high and low levels during a second operating mode;

a delay network that controls the waveform control to provide the output at the intermediate level for a duration during the first operating mode; and

a driver that provides an output signal based on the control output provided by the waveform control, the output signal transitions between associated high and low levels during the second operating mode, and transitions to an intermediate level for the duration during the first operating mode; and

the waveform control further comprising at least one component coupled to temporarily diode connect a transistor device of the driver, by providing each of a control input node and a second node of the transistor device with a matched voltage, to enable the driver to provide the output signal at the intermediate level for the duration of the first operating mode.

Claim 3 (Canceled)

4. (Currently Amended) The system of claim 3, ~~the~~, the waveform control further comprising a logic network that controls operation of the at least one component based at least in part on a delayed signal provided by the delay network.

5. (Original) The system of claim 2, the driver further comprising at least first and second inputs, the control output provided by the waveform control further comprising a